

# Prior Art

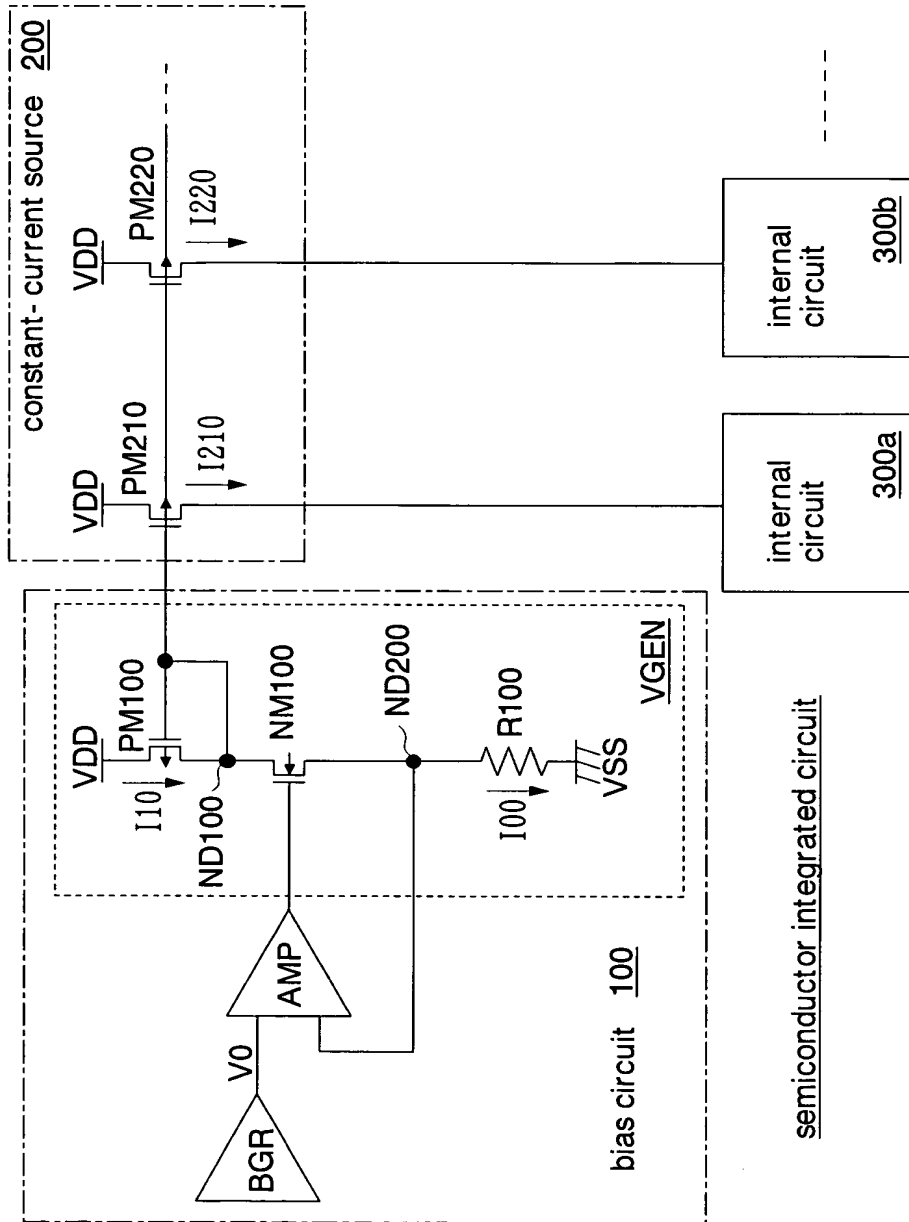


Fig. 1

# Prior Art

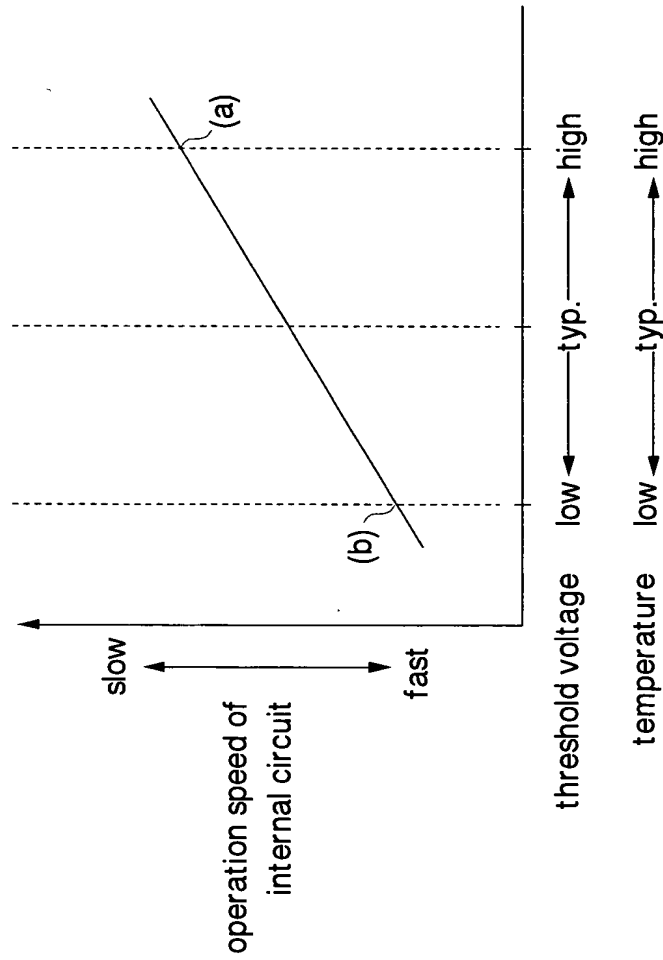


Fig. 2

## Prior Art

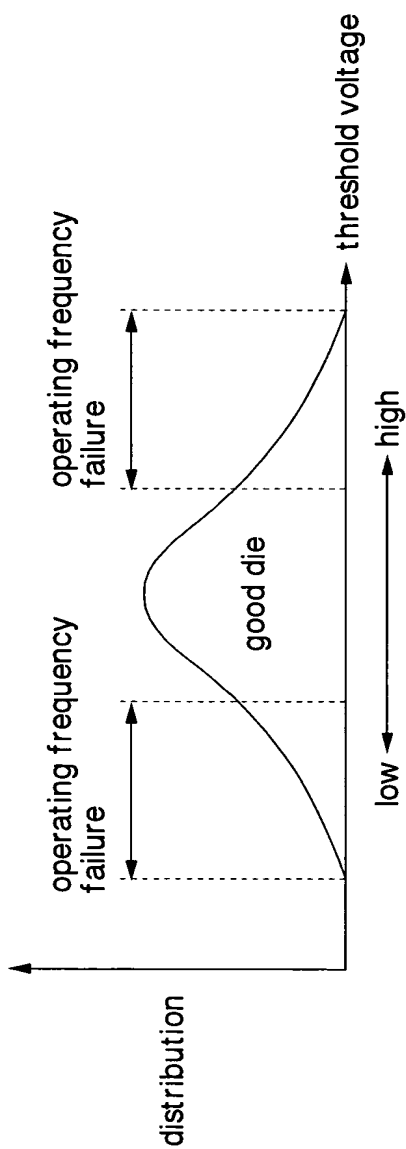
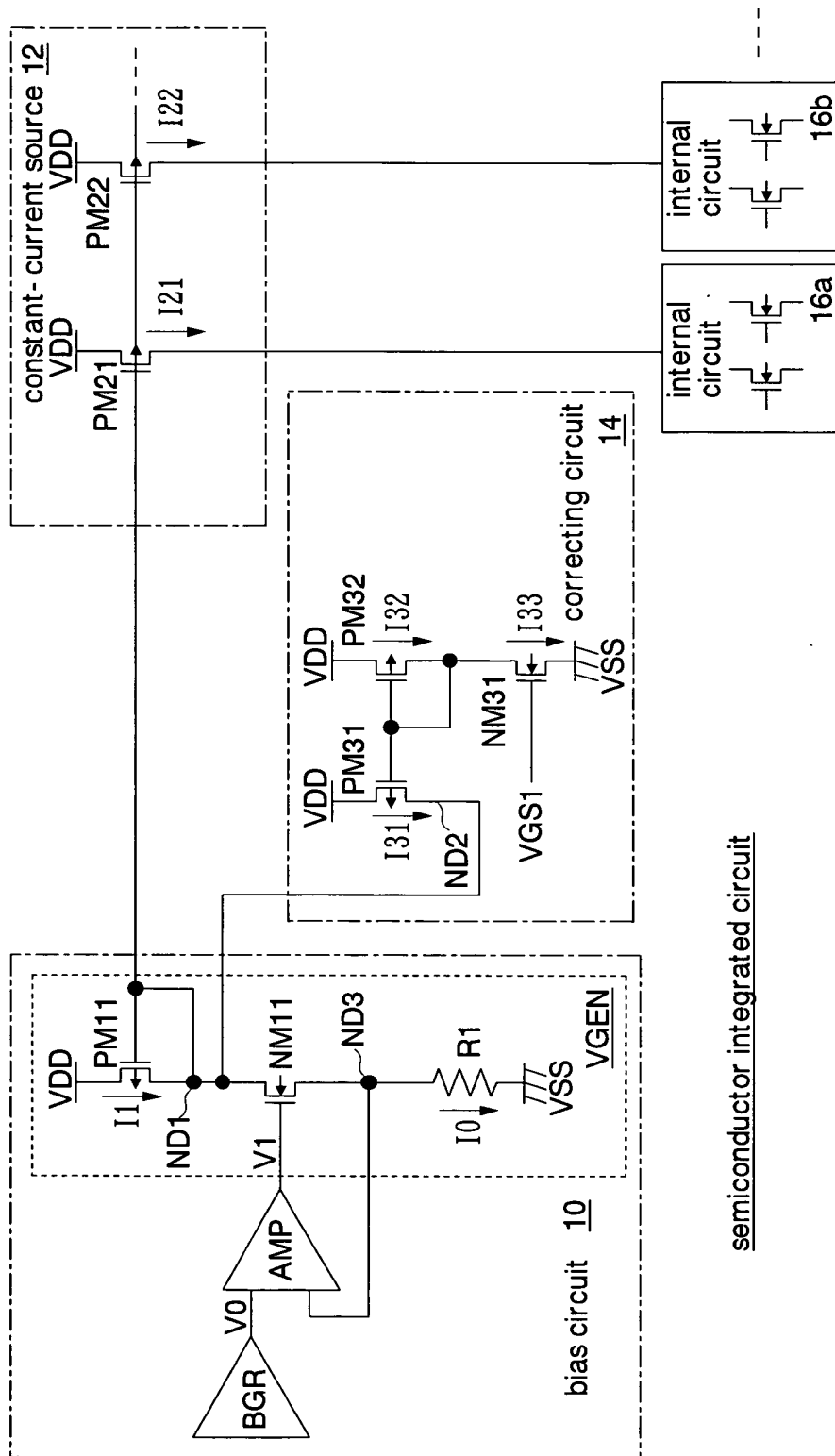


Fig. 3



semiconductor integrated circuit

Fig. 4

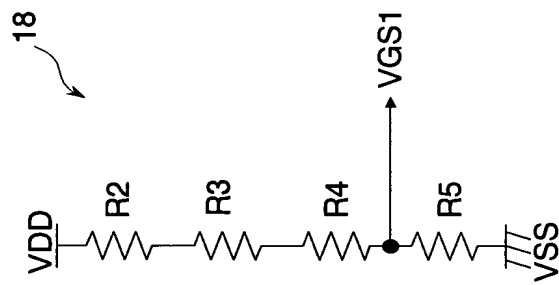


Fig. 5

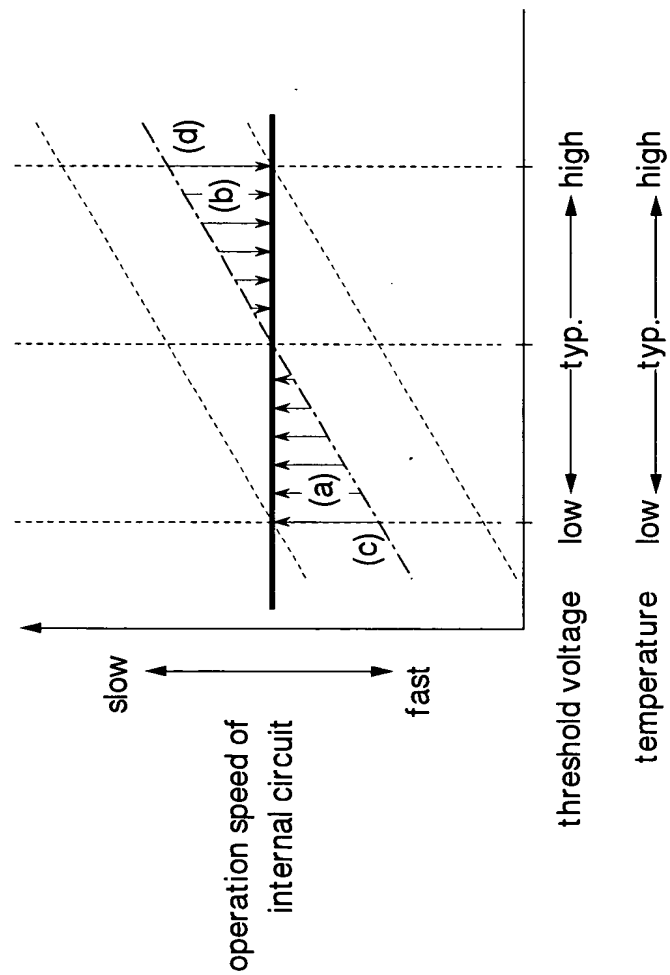


Fig. 6

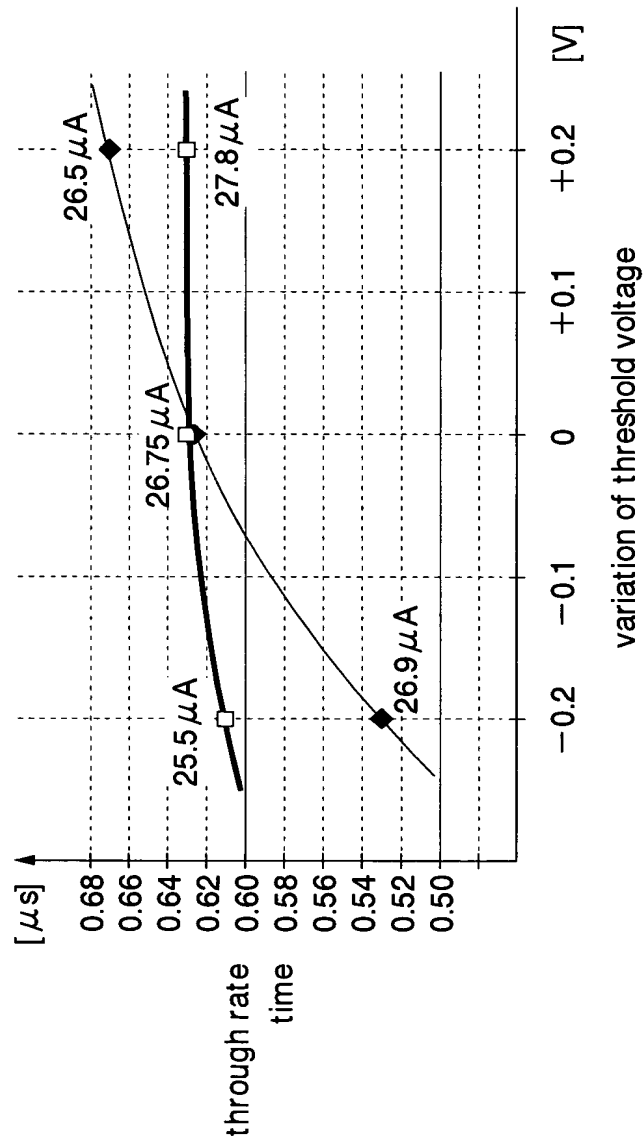


Fig. 7

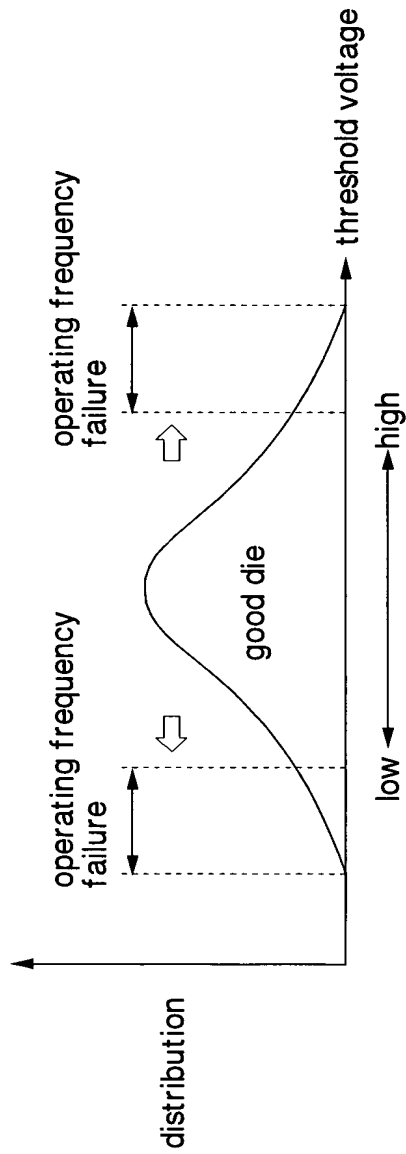


Fig. 8



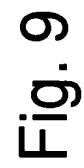


Fig. 9

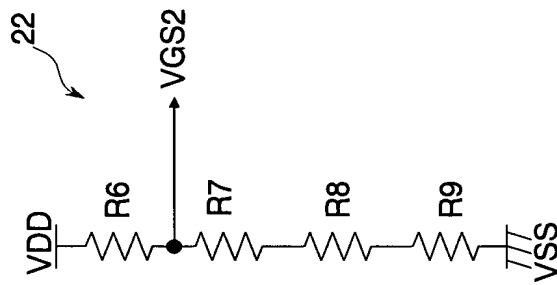


Fig. 10

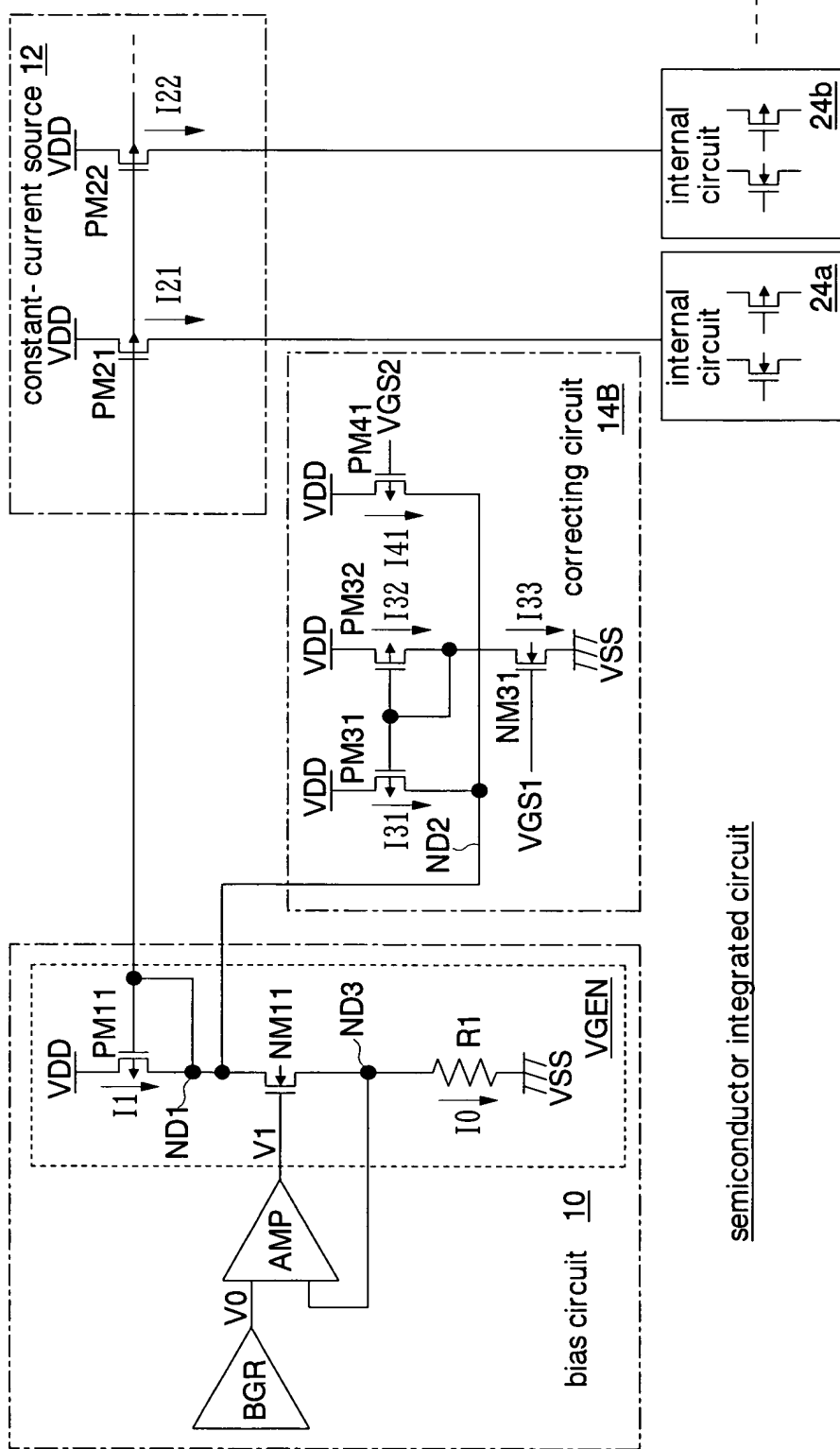


Fig. 11

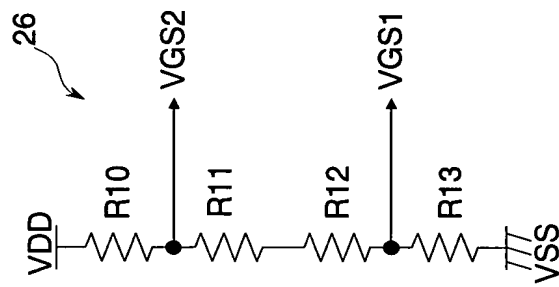


Fig. 12

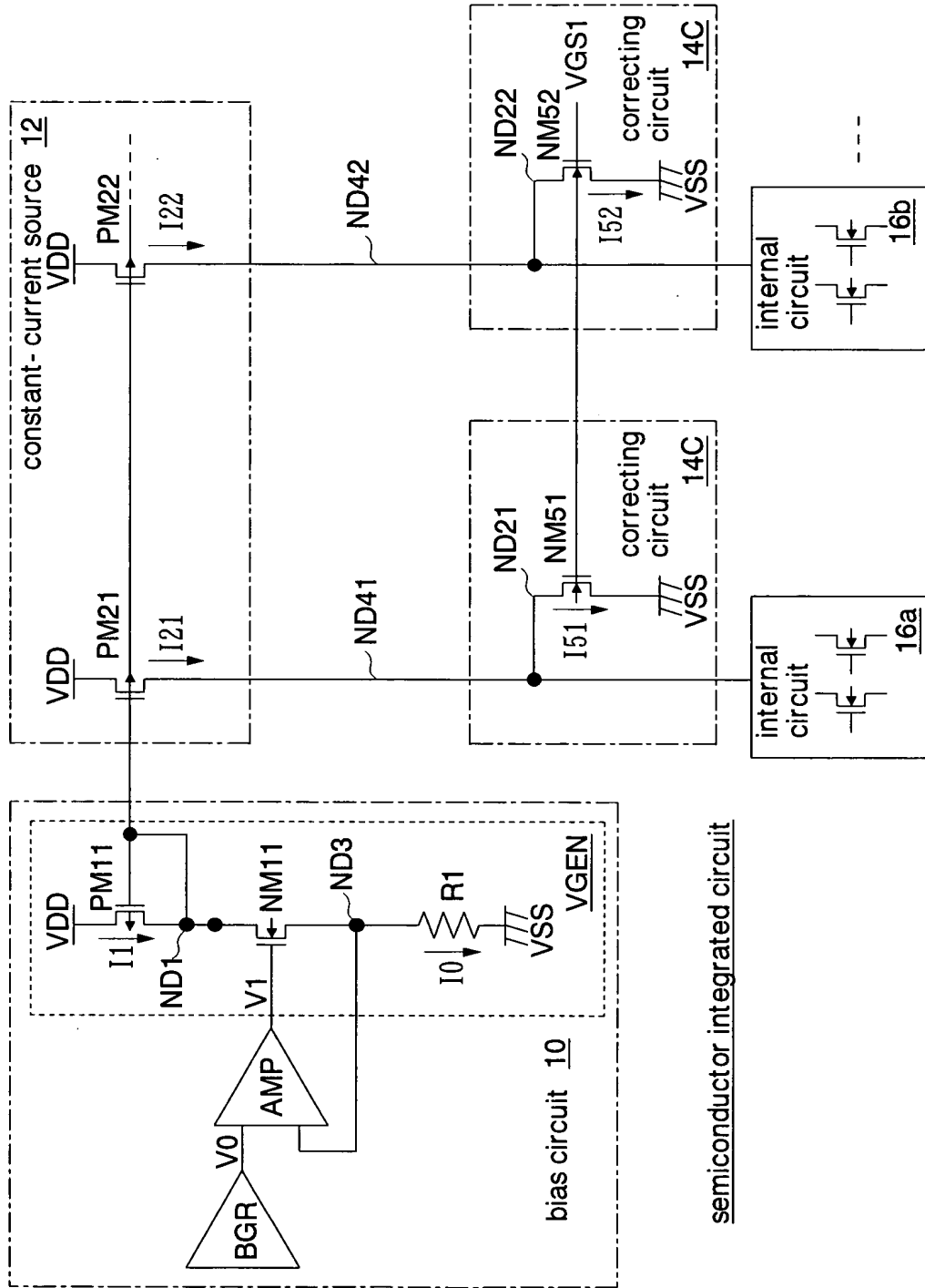


Fig. 13

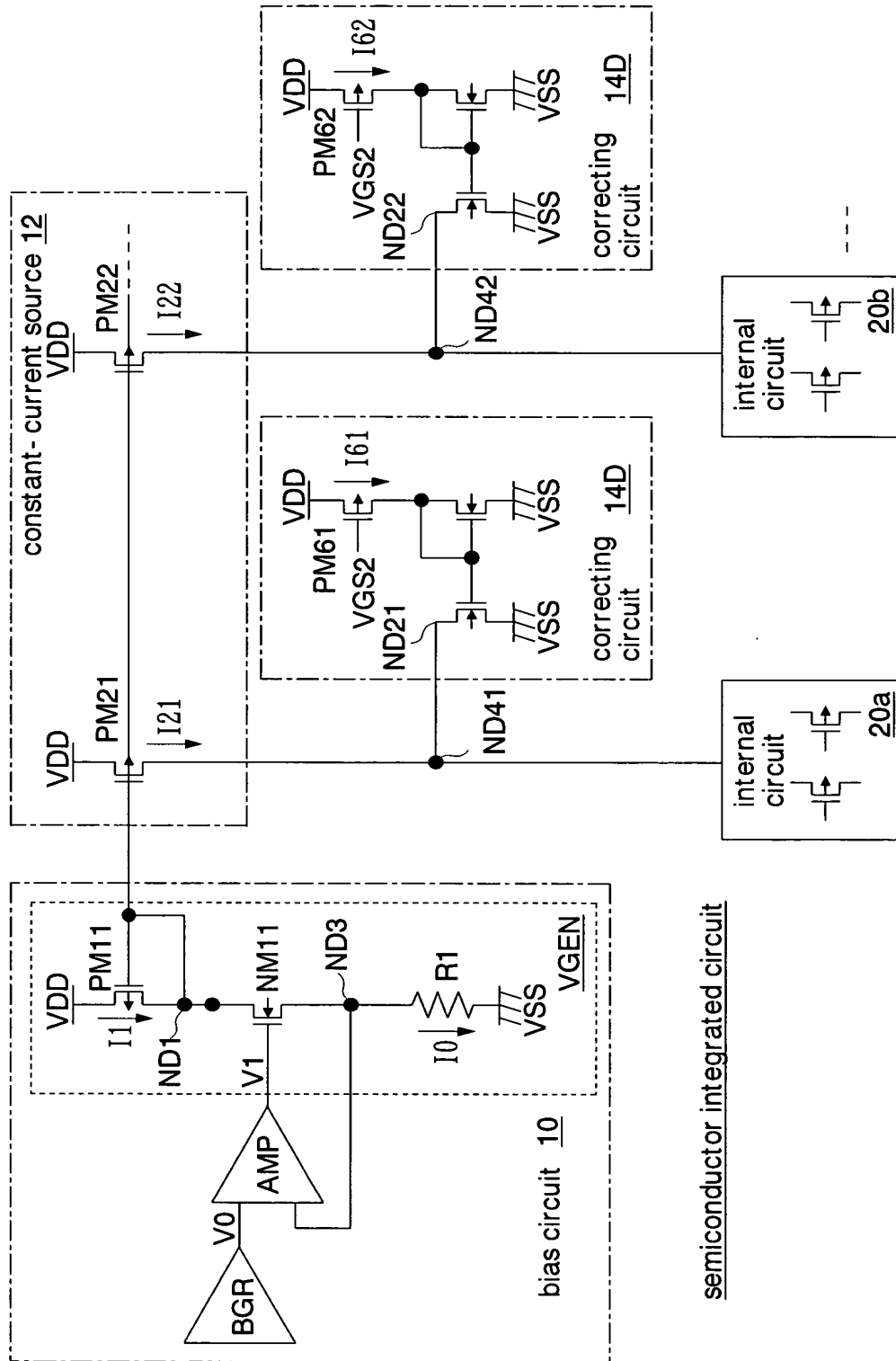


Fig. 14

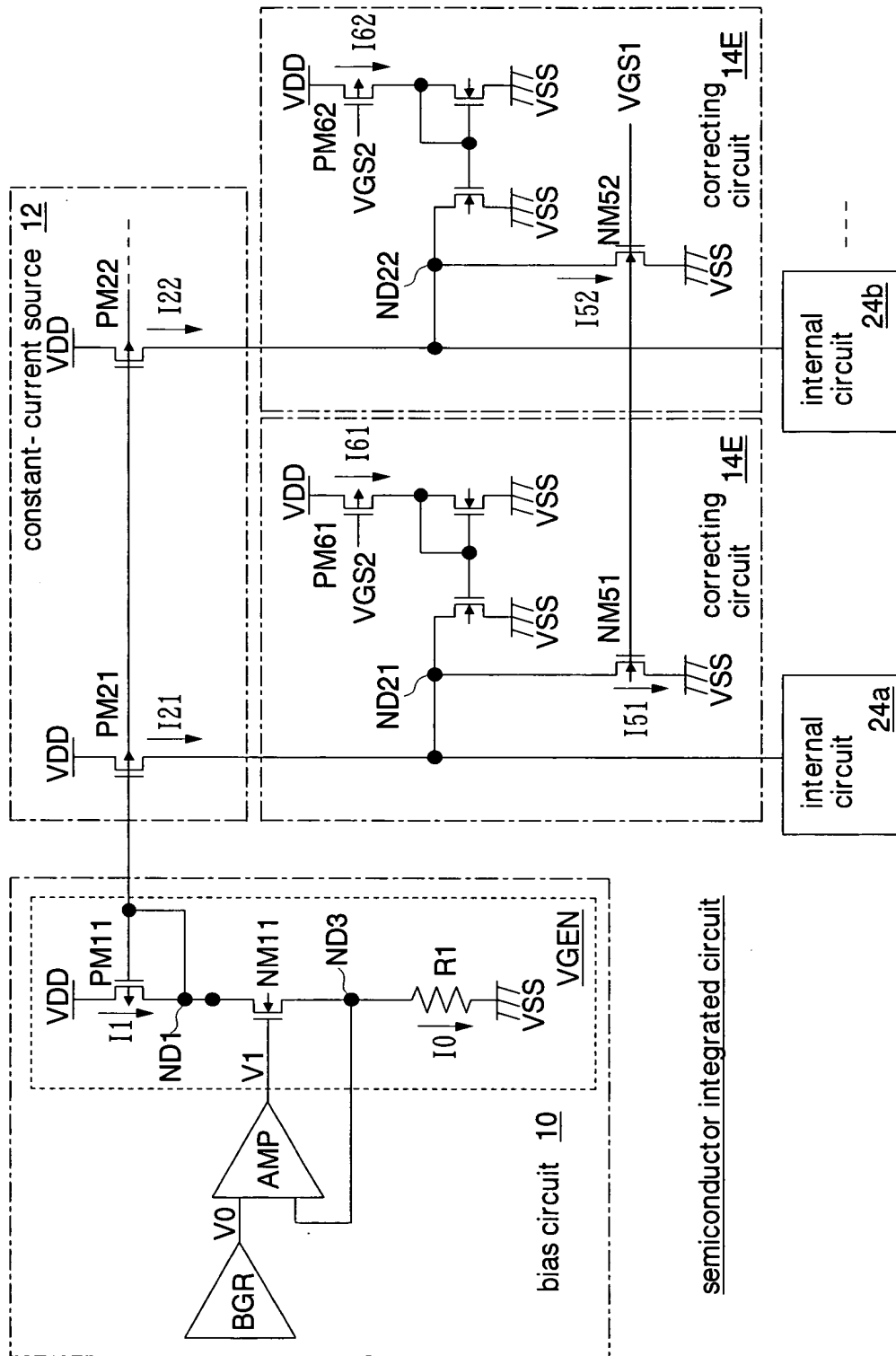


Fig. 15

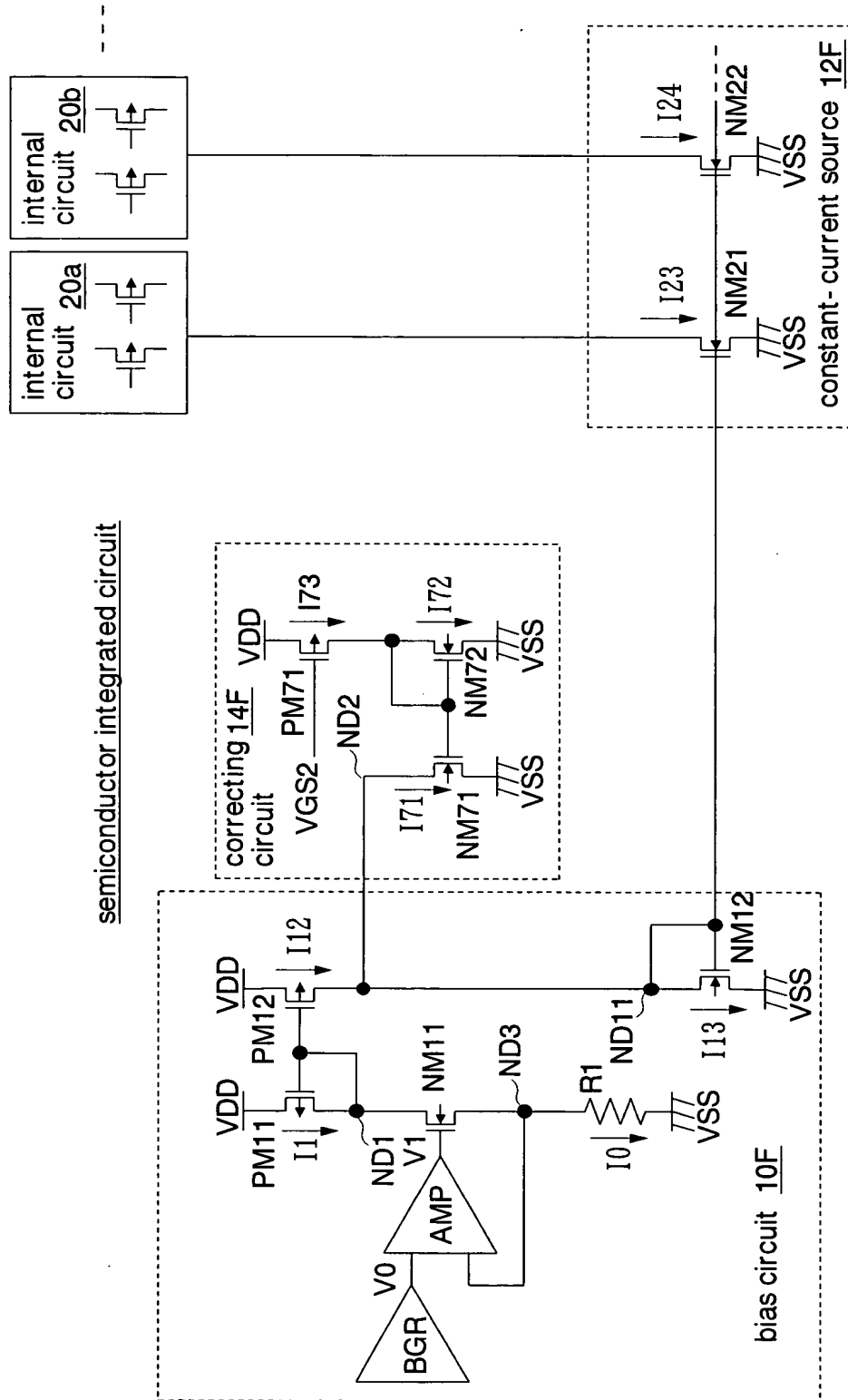


Fig. 16



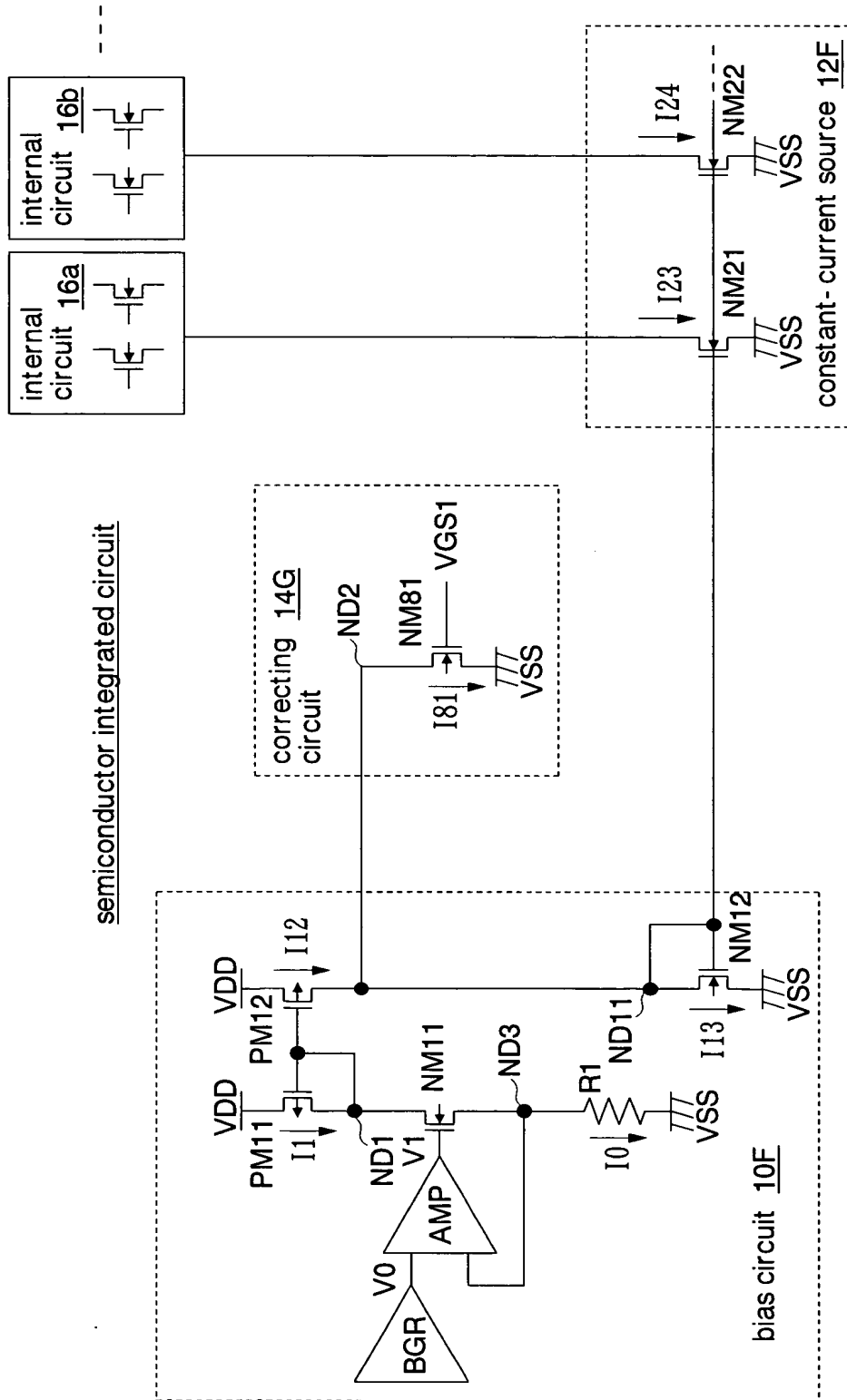


Fig. 17



**Fig. 18**

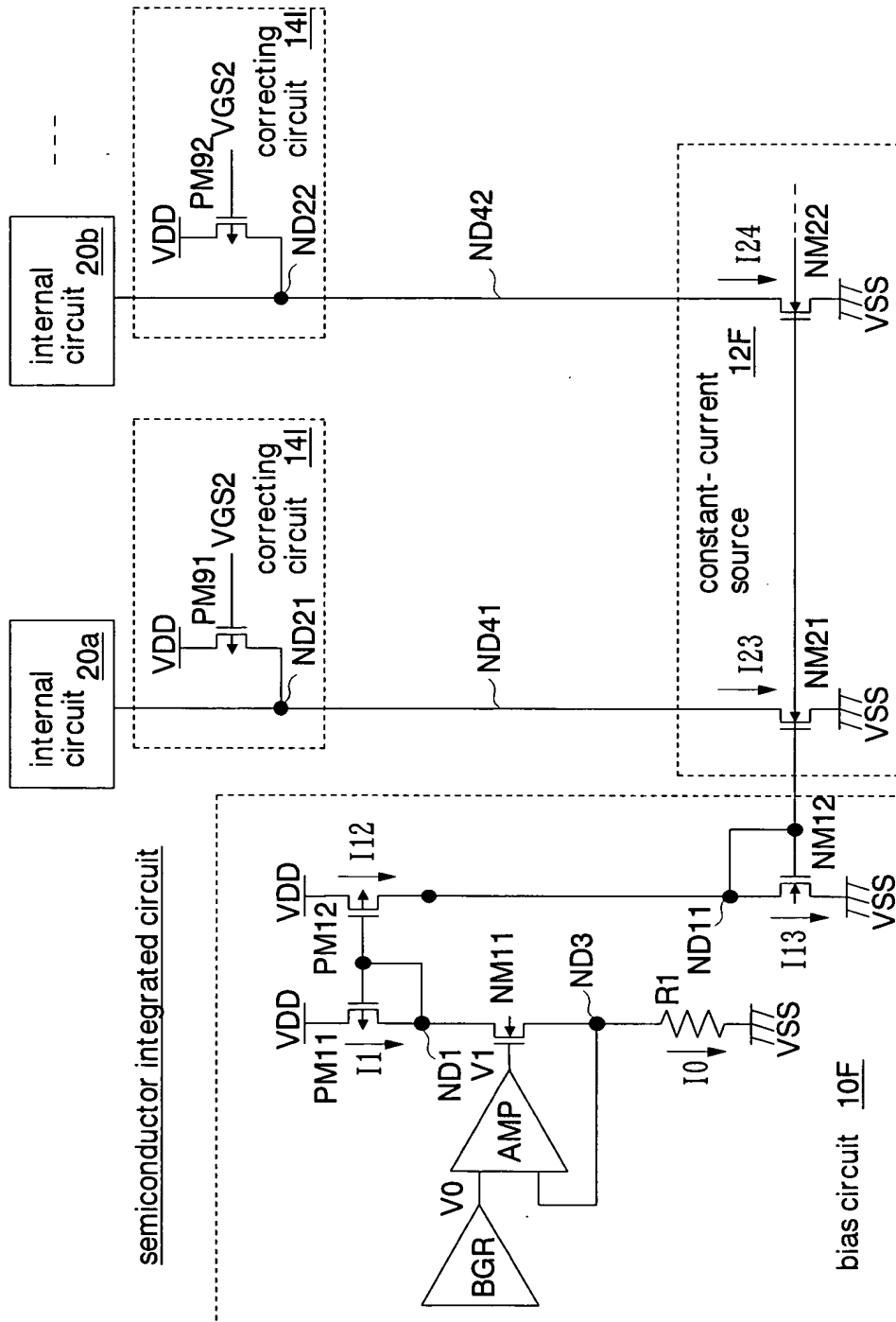


Fig. 19

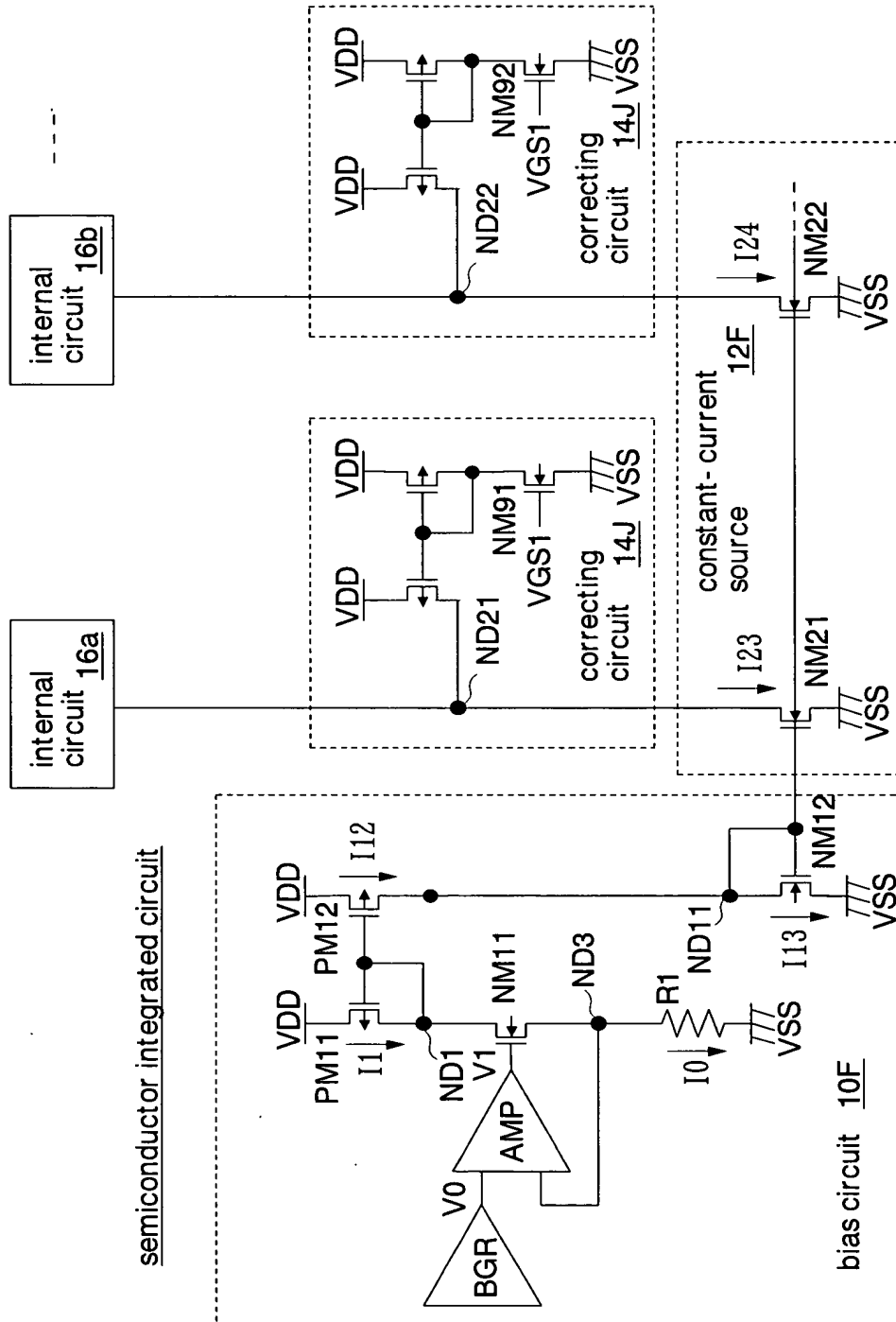


Fig. 20

